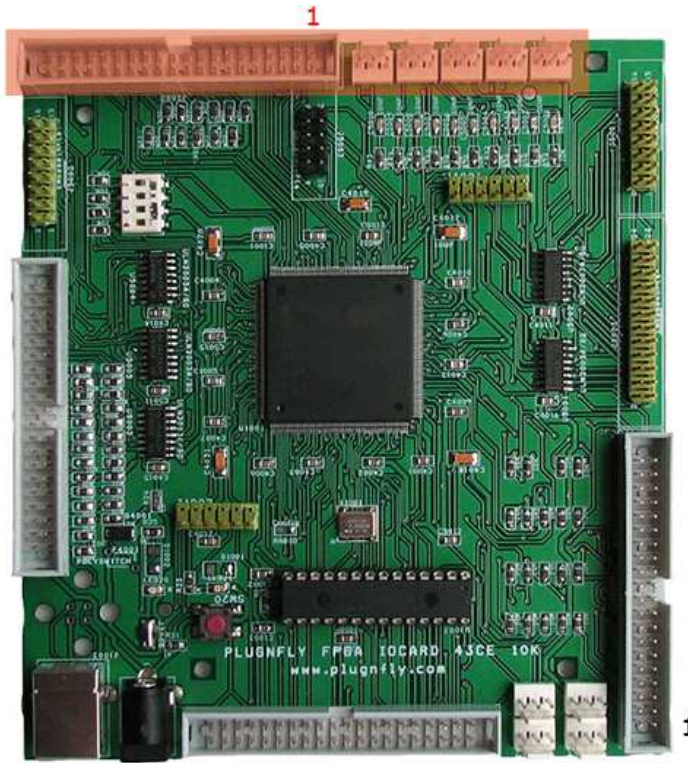
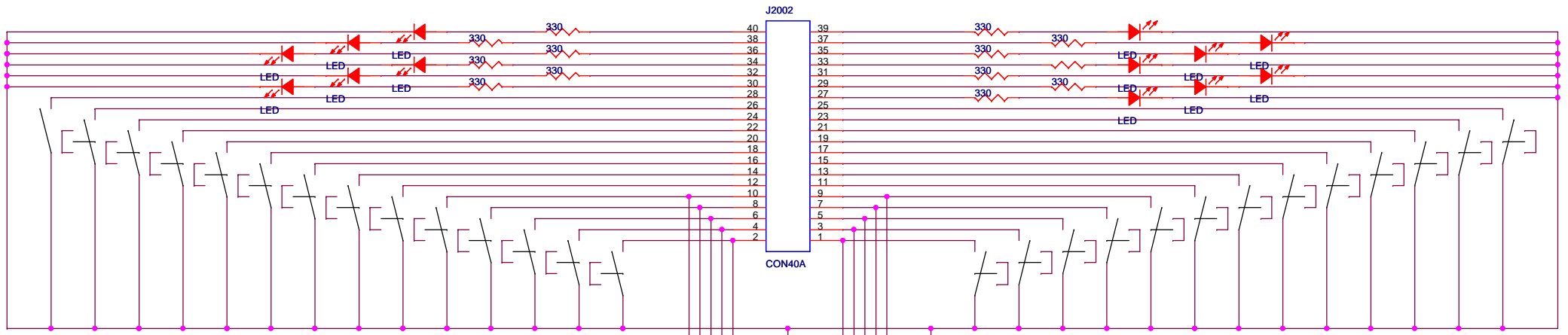


# Connecteur IOCARD\_J2002

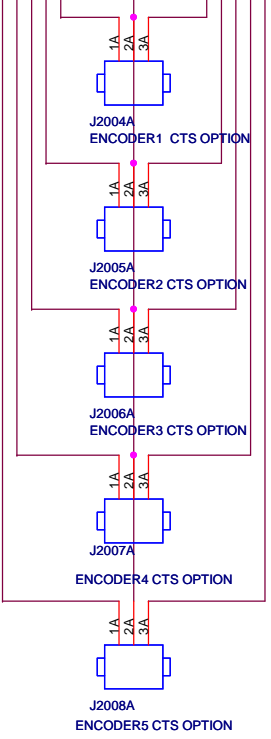


MAP OUTPUTS PINS J2002

HE10	board	board	board	board
PIN	N°1	N°2	N°3	N°4
40	37	101	165	229
39	36	100	164	228
38	35	99	163	227
37	34	98	162	226
36	33	97	161	225
35	32	96	160	224
34	31	95	159	223
33	30	94	158	222
32	29	93	157	221
31	28	92	156	220
30	27	91	155	219
29	26	90	154	218
28	25	89	153	217
27	24	88	152	216
26	23	87	151	215

MAP INPUTS PINS J2002

HE10	board	board	board	board
PIN	N°1	N°2	N°3	N°4
28	80	144	208	272
26	81	145	209	273
25	83	147	211	275
24	82	146	210	274
23	87	151	215	279
21	79	143	207	271
20	85	149	213	277
19	84	148	212	276
18	71	135	199	263
17	72	136	200	264
16	74	138	202	266
15	73	137	201	265
14	78	142	206	270
13	77	141	205	269
12	70	134	198	262
11	76	140	204	268
10	75	139	203	267
9	65	126	190	254
8	63	127	191	255
7	65	129	193	257
6	64	128	192	256
5	69	133	197	261
4	68	132	196	260
3	61	125	189	253
2	67	131	195	259
1	66	130	194	258



TIP: If the CPLD board have different firmware of IOCARD64, Assigment of inputs/outputs have a different mapping.